

FIG. 1

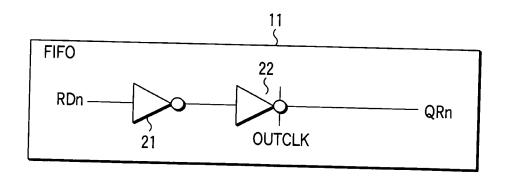
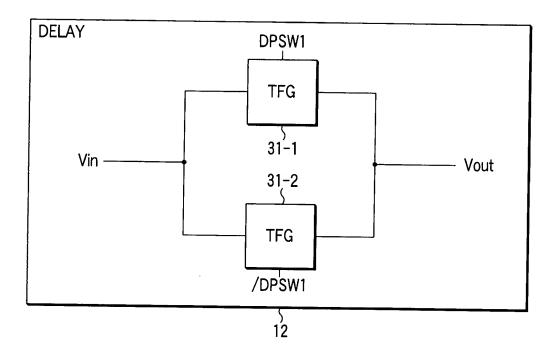
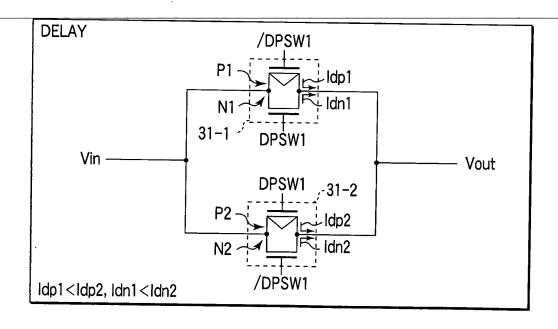


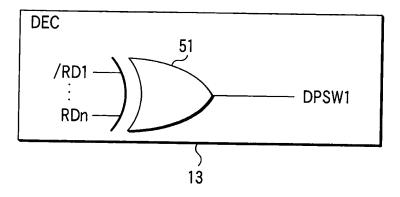
FIG.2



F I G. 3



F I G. 4



F I G. 5

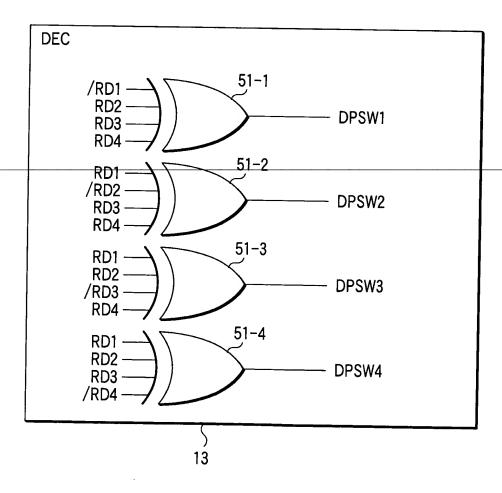
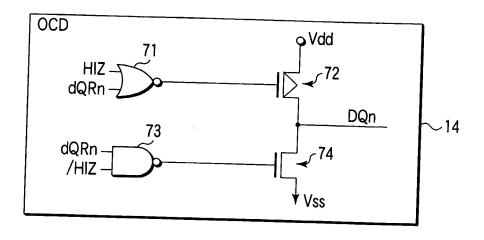
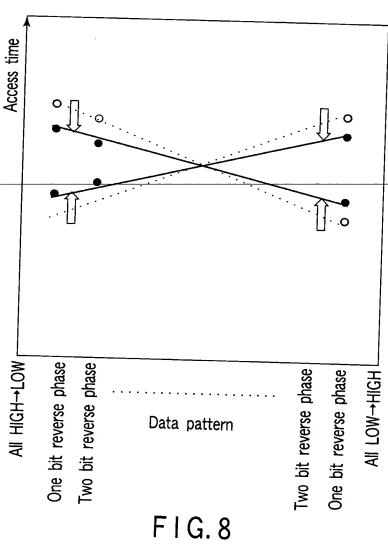


FIG.6



F1G.7



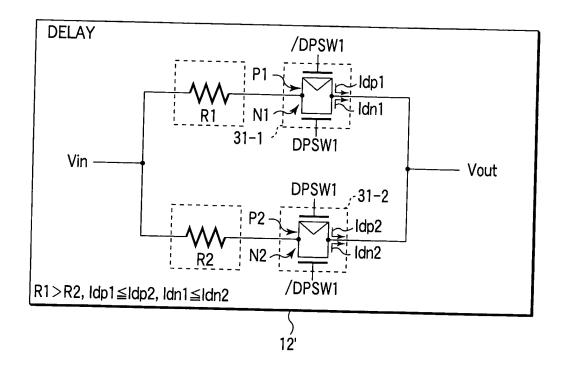


FIG. 9

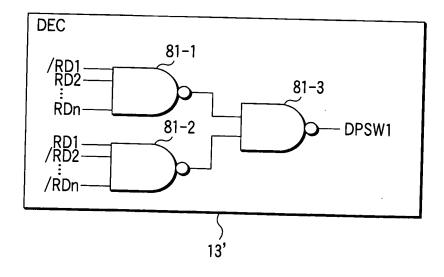


FIG. 10

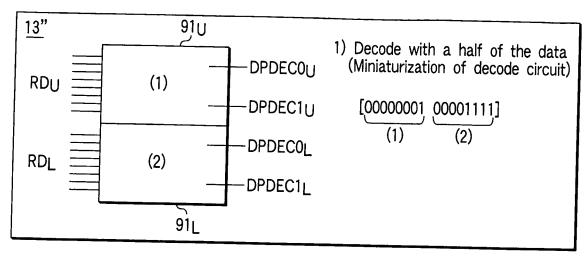
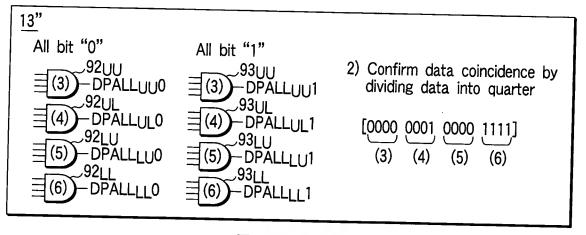


FIG. 11A



F | G. 11B

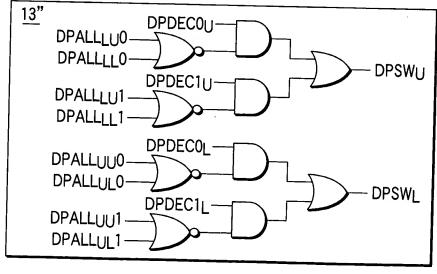


FIG. 11C

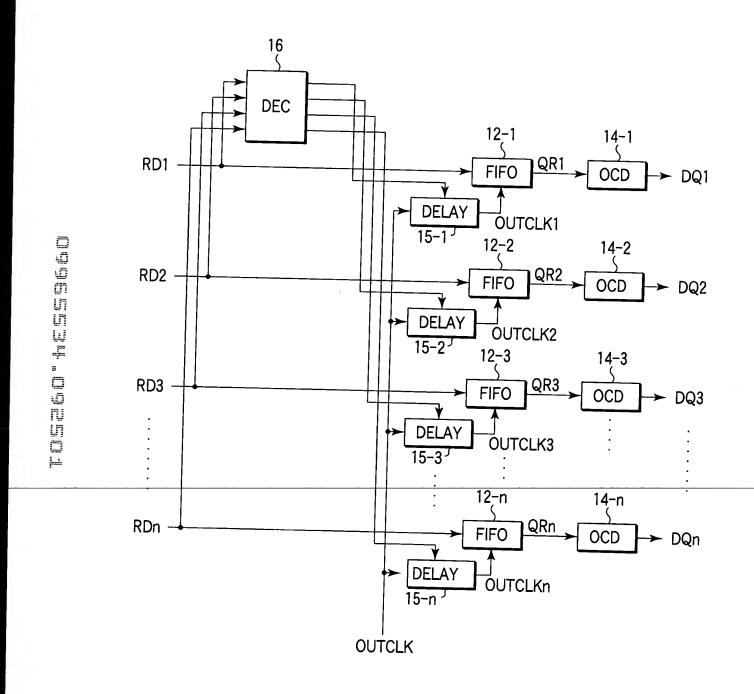
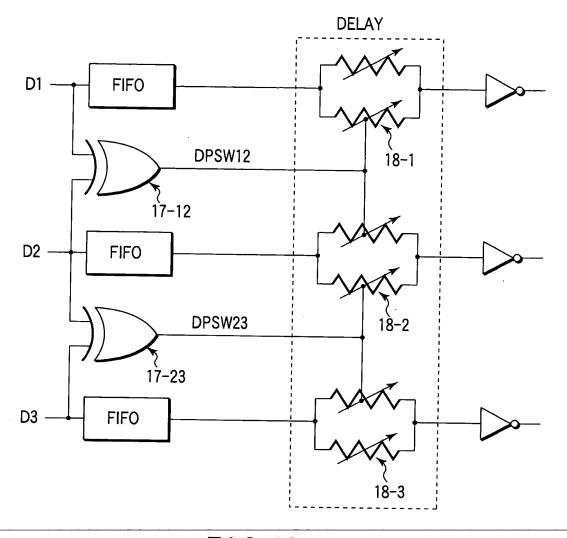


FIG. 12



F I G. 13

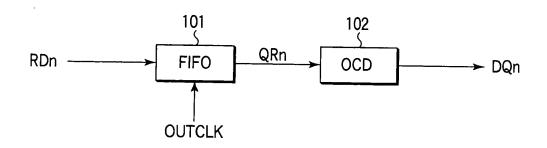


FIG. 14 PRIOR ART

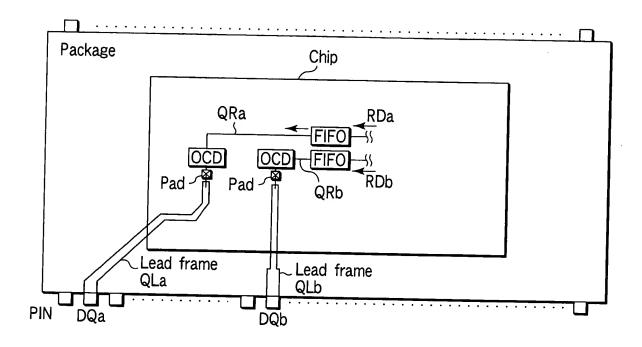


FIG. 15A PRIOR ART

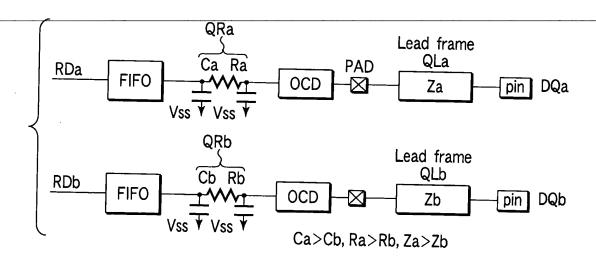


FIG. 15B PRIOR ART

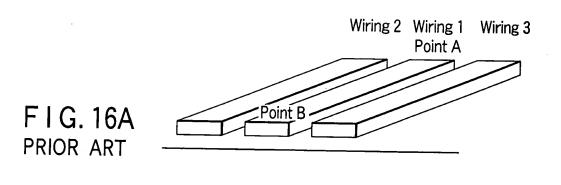


FIG. 16B PRIOR ART

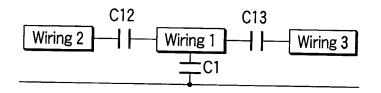
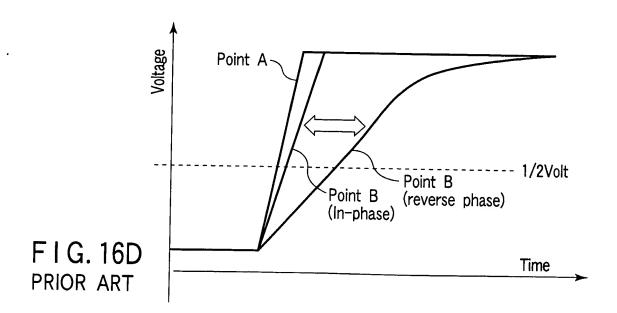


FIG. 16C

	Wiring 2	Wiring 1	Wiring 3	Data pattern
Potential change	1	1	1	In-phase
	1	1	1	Reverse phase
	1	1	1	In-phase
<u>a</u>	1		1	Reverse phase

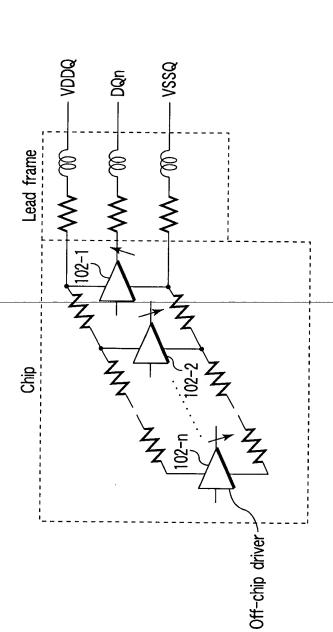


Potential change  $(\Delta V)$  by the transient current which flows the power source GND

ΔV=N · Leff · (di/dt)

Effective inductance (Leff) of PKG (power source) Current drive performance (di/dt) of the driver

Simultaneous switching number (N)



F | G. 17 PRIOR ART

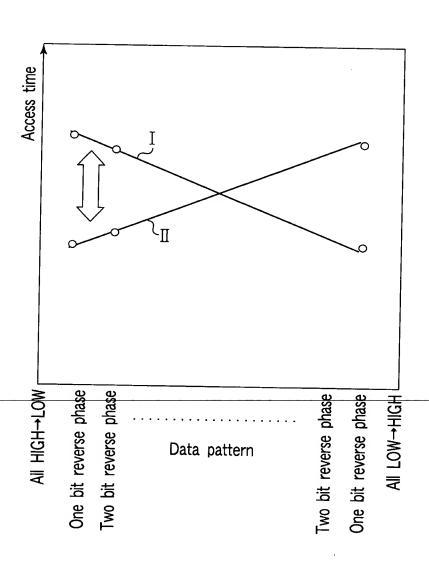


FIG. 18 PRIOR ART